| | Application No. | Applicant(s) |
|---|--|---|
| Notice of Allowability | 10/771,823 | LIN ET AL. |
| | Examiner | Art Unit |
| | Mujtaba K. Chaudry | 2133 |
| The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R | (OR REMAINS) CLOSED in this app or other appropriate communication IGHTS. This application is subject to | olication. If not included will be mailed in due course. THIS |
| 1. X This communication is responsive to interview on 1/18/200 | <u>07</u> . | |
| 2. ☑ The allowed claim(s) is/are <u>1-30</u> . | | |
| 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have | e been received. | |
| 3. Copies of the certified copies of the priority do | · · · · · · · · · · · · · · · · · · · | |
| International Bureau (PCT Rule 17.2(a)). | | • |
| * Certified copies not received: | | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | of this communication to file a reply IENT of this application. | complying with the requirements |
| 4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | itted. Note the attached EXAMINER es reason(s) why the oath or declara | S AMENDMENT or NOTICE OF tion is deficient. |
| 5. CORRECTED DRAWINGS (as "replacement sheets") mus | st be submitted. | |
| (a) \square including changes required by the Notice of Draftspers | | 948) attached |
| 1) hereto or 2) to Paper No./Mail Date | | |
| (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date | s Amendment / Comment or in the C | Office action of |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t | .84(c)) should be written on the drawing the header according to 37 CFR 1.121(| ngs in the front (not the back) of d). |
| DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | | |
| | | , |
| Attachment(s) | | |
| Notice of References Cited (PTO-892) D Notice of Draftperson's Patent Drawing Review (PTO-948) | 5. ☐ Notice of Informal P 6. ☒ Interview Summary | |
| 3. ☐ Information Disclosure Statements (PTO/SB/08), | Paper No./Mail Dat 7. 🛛 Examiner's Amendr | |
| Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit | 8. 🛛 Examiner's Stateme | ent of Reasons for Allowance |
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Art Unit: 2133

DETAILED ACTION

Page 2

Applicants' response to Exparte Quayle Action was received December 1, 2006.

- Claims 6 and 25 are amended herein as discussed.

- The corrected oath is accepted.

- Interview summary attached.

Application allowed.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Anthony M. Palagonia on Thursday, January 18, 2007.

Please amend the application as follows:

Please replace claim 6 with:

6. (Currently Amended) The CRC generator/checker of claim 5, wherein:

Art Unit: 2133

each multi-byte CRC is adapted to process a different number of bytes of data selected from the series of powers of two bytes in the range 2^(N-1) to 2^Y where 2^(N-1) is equal to a number of said CRC circuits in said set of CRC circuits and Y is less than (N-1); and

Page 3

wherein Y is chosen such the number of concatenated multi-byte CRC calculations performed plus {W-[2^(N-1)+2^(N-2)+...+2^Y]} concatenated 1-byte CRC calculations can be performed in a single clock cycle and where W is the width in bytes of a data bus supplying said data unit and the number of CRC circuits.

Please replace claim 25 with:

25. (Currently Amended) The method of claim 24, wherein:

each multi-byte CRC is adapted to process a different number of bytes of data selected from the series of powers of two bytes in the range 2^(N-1) to 2^Y where 2^(N-1) is equal to a number of said CRC circuits in said set of CRC circuits and Y is less than (N-1); and

further including choosing Y such the number of concatenated multi-byte CRC calculations performed plus {W-[2^(N-1)+2^(N-2)+...+2^Y]} concatenated 1-byte CRC calculations can be performed in a single clock cycle and where W is the width in bytes of a data bus supplying said data unit and the number of CRC circuits.

Art Unit: 2133

REASONS FOR ALLOWANCE

Claims 1-30 are allowed. The following is a statement of reasons for allowance:

Independent claim 1 of the present application teaches a CRC generator/checker for generating CRC results, comprising: a set of CRC circuits connected in series, each CRC circuit responsive to a different control signal generated by a control logic, each CRC circuit having a seed input adapted to receive a seed, a data input adapted to receive and process a different set of M-bits of a data unit and a result output adapted to generate a result, the result output of a previous CRC circuit connected to the seed input of an immediately subsequent CRC circuit, the seed input of a first CRC circuit connected to an output of a remainder register, an input of said remainder register connected to an output of a multiplexer, the result outputs of said set of CRC circuit connected to different inputs of said multiplexer, said multiplexer responsive to a select signal generated by said control logic. The foregoing limitations are not found in the prior arts of record. Particularly, none of the prior arts of record teach nor fairly suggest, "...a set of CRC circuits connected in series, each CRC circuit responsive to a different control signal generated by a control logic, each CRC circuit having a seed input adapted to receive a seed, a data input adapted to receive and process a different set of M-bits of a data unit and a result output adapted to generate a result, the result output of a previous CRC circuit connected to the seed input of an immediately subsequent CRC circuit, the seed input of a first CRC circuit connected to an output of a remainder register, an input of said remainder register connected to an output of a multiplexer, the result outputs of said set of CRC circuit connected to different inputs of said multiplexer, said multiplexer responsive to a select signal generated by said control logic."

Art Unit: 2133

Independent claims 12 and 20 recite similar limitations of independent claim 1 and therefore are allowed for similar reasons.

Dependent claims 2-11, 13-19 and 21-30 depend from allowable independent claims and inherently include limitations therein and therefore are allowed as well.

Page 5

Art Unit: 2133

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mujtaba K. Chaudry whose telephone number is 571-272-3817.

The examiner can normally be reached on Mon-Thur 9-7:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Albert DeCady can be reached on 571-272-3819. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

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Art Unit 2133

January 18, 2007

Page 6